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PPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/088,674	06/02/1998	DANIEL J. MORGAN	TI-25995	2025
23494 7590 11/13/2003			EXAMINER	
TEXAS INST	TRUMENTS INCORPO	NGUYEN, KEVIN M		
P O BOX 6554 DALLAS, TX			ART UNIT	PAPER NUMBER
<i>Ditabli</i> 17	73203		2674	2 ~
			DATE MAIL ED. 11/12/2003	25

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati	on No.	Applicant(s)			
Office Action Summary				MORGAN ET AL.			
		09/088,6		Art Unit			
		Examine		2674			
	The MAILING DATE of this communication	Kevin M.					
Period fo		on appears on an					
THE - External after - If the - If NO - Failu - Any	ORTENED STATUTORY PERIOD FOR R MAILING DATE OF THIS COMMUNICATI msions of time may be available under the provisions of 37 C SIX (6) MONTHS from the mailing date of this communicati e period for reply specified above is less than thirty (30) days o period for reply is specified above, the maximum statutory are to reply within the set or extended period for reply will, by reply received by the Office later than three months after the ed patent term adjustment. See 37 CFR 1.704(b).	ION. CFR 1.136(a). In no eviction. s, a reply within the star period will apply and way statute, cause the appropriate the appropriate in the appr	ent, however, may a reply be tutory minimum of thirty (30) of till expire SIX (6) MONTHS for blication to become ABANDO	timely filed days will be considered timely. om the mailing date of this communication. NED (35 U.S.C. § 133).			
1)⊠	Responsive to communication(s) filed on	04 August 2003	<u>3</u> .				
2a) <u></u> ☐	This action is FINAL . 2b)⊠ This action is non-final.						
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
4)⊠	☑ Claim(s) <u>1-10</u> is/are pending in the application.						
	4a) Of the above claim(s) is/are withdrawn from consideration.						
5)□	Claim(s) is/are allowed.						
•	Claim(s) <u>1-10</u> is/are rejected.						
7)[Claim(s) is/are objected to.						
8)[Claim(s) are subject to restriction a	and/or election r	equirement.				
Applicat	ion Papers						
9) The specification is objected to by the Examiner.							
10)[10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.						
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).						
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).						
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority (under 35 U.S.C. §§ 119 and 120						
a)	Acknowledgment is made of a claim for for All b) Some * c) None of: 1. Certified copies of the priority docu 2. Certified copies of the priority docu 3. Copies of the certified copies of the application from the International B	iments have bee iments have bee e priority docum Bureau (PCT Rul	en received. en received in Applica ents have been rece e 17.2(a)).	ation No ived in this National Stage			
13) <u> </u>	See the attached detailed Office action for Acknowledgment is made of a claim for do ince a specific reference was included in to 7 CFR 1.78. I) The translation of the foreign language.	mestic priority u he first sentence	nder 35 U.S.C. § 119 of the specification	9(e) (to a provisional application) or in an Application Data Sheet.			
14) 🗌 A	Acknowledgment is made of a claim for do eference was included in the first sentence	mestic priority u	nder 35 U.S.C. §§ 12	20 and/or 121 since a specific			
Attachmen	it(s)						
2) 🔲 Notic	ce of References Cited (PTO-892) ce of Draftsperson's Patent Drawing Review (PTO-94 mation Disclosure Statement(s) (PTO-1449) Paper N			ary (PTO-413) Paper No(s) I Patent Application (PTO-152)			

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DETAILED ACTION

1. In view of the appeal brief filed on 8/4/2003, PROSECUTION IS HEREBY REOPENED. A rebuttal to the Reply Brief is set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

- (1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37 CFR 1.113 (if this Office action is final); or,
 - (2) request reinstatement of the appeal.

If reinstatement of the appeal is requested, such request must be accompanied by a supplemental appeal brief, but no new amendments, affidavits (37 CFR 1.130, 1.131 or 1.132) or other evidence are permitted. See 37 CFR 1.193(b)(2).

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- 3. Claims 1-10 are rejected under 35 U.S.C. 102(e) as being anticipated by Yamaguchi et al (US 6,222,515).

As to claims 1 and 6, Yamaguchi et al teaches a system of displaying digital video data associated with a method comprising a logic circuit offsetting a first pixel

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value a first predetermined amount (2V, 4V) to form a first offset pixel value, said logic circuit (4) also offsetting said first said pixel value by the opposite of said first predetermined amount (-2V, -4V) to form a second offset value; and

display panel (19) displaying said first offset pixel value during a first display frame "a positive frame" and displaying said second offset pixel value during a second display frame "a negative frame", such that the average of said displayed first offset pixel value and said second offset pixel value is said first pixel value "said means effective voltage of (±3V) is shown by hatching in figure 7B" (see figures 1A, 1B, 7B, column 5, lines 58-64 and column 8, lines 11-27).

As to claims 2 and 7, Yamaguchi et al teaches the system associated the method comprising the value of said first predetermined amount (2V, 4V) is selected by said logic circuit as a function "average" of said first pixel value "said means effective voltage of 3V" (see figures 1A, column 5, lines 58-64).

As to claims 3 and 8, Yamaguchi et al teaches the system associated the method comprising said first offset pixel value (±2V, ±4V) is greater than or less than said first pixel value (3V) as a function "average" of the spatial location that said first pixel value "said means effective voltage of (±3V)" is to be displayed (see figures 1A, 7B, column 8, lines 11-27).

As to claims 4 and 9, Yamaguchi et al teaches the system associated with the method comprising said pixel value are displayed using a plurality of weighted bit-planes "a first field memory (13), a second field memory (14)", wherein said first pixel values close to a bit transition of said bit-planes (13, 14) are offset during said first

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display frame "said positive frame" and said second frame "said negative frame" (see figure 1B, column 6, lines 41-55).

As to claims 5 and 10, Yamaguchi et al teaches the system associated with the method comprising said first display frame "said positive frame and said second display frame "said negative frame" are consecutive (see figure 7B, column 8, lines 11-27).

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Kevin M. Nguyen** whose telephone number is **703-305-6209**. The examiner can normally be reached on MON-THU from 9:00-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard A Hjerpe** can be reached on **703-305-4709**.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered response should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth floor (Receptionist).

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Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

Kevin M. Nguyen Patent Examiner Art Unit 2674

KN

November 11, 2003

RICHARD HJERPE SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2600